

## **ABSTRACT OF THE DISCLOSURE**

A device and method for recording the block status information of a nonvolatile memory is disclosed. An interface unit is used to host the nonvolatile memory so that a processor connected to the interface unit can  
5 detect the block status information of the nonvolatile memory through the interface unit to obtain a valid or invalid block address. Then, the valid or invalid block address is temporarily stored in a memory unit until all of the blocks are detected by means of the processor. Finally, the valid or invalid  
10 block address temporarily stored in the memory unit is written into the nonvolatile memory.